



RCE/2800

REQUEST FOR CONTINUED EXAMINATION (RCE) TRANSMITTAL Subsection (b) of 35 U.S.C. § 132, effective on May 29, 2000, provides for continued examination of an utility or plant application filed on or after June 8, 1995. See The American Inventors Protection Act of 1999 (AIPA).	<i>Application Number</i>	09/256,643
	<i>Filing Date</i>	February 23, 1999
	<i>First Named Inventor</i>	Leonard Forbes et al.
	<i>Group Art Unit</i>	2822
	<i>Examiner Name</i>	Michael Trinh
	<i>Attorney Docket Number</i>	303.324US2

This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application entitled TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS OF FABRICATION AND USE.

Submission required under 37 C.F.R. § 1.114

1. ☐ Consider the amendment(s)/reply under 37 C.F.R. § 1.116 previously filed on ☐
2. ☐ Consider the arguments in the Appeal Brief or Reply Brief previously filed on ☐
3. ☒ A Response Under 37 CFR § 1.116 (5 pages) is enclosed.
4. ☐ A new power of attorney (☐ pages) is enclosed.
5. ☐ An Information Disclosure Statement is enclosed (☐ pages)
 - a. ☐ Form(s) 1449
 - b. ☐ Copies of IDS Citations
6. ☒ A check in the amount of \$740.00 is attached to pay the RCE filing fee required under C.F.R. § 1.17(e).
7. ☒ The Commissioner is hereby authorized to credit overpayments or charge any fees set forth in 37 C.F.R. §§ 1.16 through 1.18 to Deposit Account No. 19-0743.
8. ☒ A petition for extension of time in the prior application (1 pages) is enclosed along with a check in the amount of \$110.00 to pay the extension fee.
9. ☐ Other: ☐

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Customer Number **21186**

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Box RCE, Washington, D.C. 20231, on this 11th day of July, 2002.

Name *Amy Moriarty*

Signature *Amy Moriarty*